**Generic Multi-Peripheral Core FPGA Configuration Package**

The Generic Multi-Peripheral FPGA Configuration Package is a pre-assembled Altera QSYS package providing users with pre-built and customizable FPGA fabrics for large combinations of standard interfaces including:

* RGMII/SGMII
* I2C
* SPI
* UART and 16550
* PCM Audio

The package includes a complete driver suite for interface configuration and communications and a test application to demonstrate and verify the standard interfaces.  Users may implement the package as is or customize the FPGA QSYS package to add additional features or remove functionality to reduce power consumption.



**® Preferred Partner**

# FPGA IP Cores

# ClearPHY

# Communications

**Systems Analysis and Simulation**

Matlab, Simulink, SPW, C/C++, SystemC

**General Purpose Processor**

C/C++/assmbly, Eclipse, GCC, Greenhills Multi, Microsoft Visual Studio, PPC, Intel, ARM, Microblaze, Motorolla, QNX, VxWorks, Integrity, Linux, PSOS, Nucleus Plus, Android

**Digital Signal Processor**

Texas Instruments Code Composer Studio 3.x and 5.x, 'C6416, 'C6455, DSP/BIOS

**ASIC/FPGA**

VHDL, Verilog, SystemC, Simulink, Xilinx Spartan, Virtex, Zynq Xilinx ISE, Vivado, System Generator, HDL Coder, Altera Cyclone, Stratix , Altera Quartus, Timequest, Synplify Modelsim



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